



WR10, WR12, WR08, WR06, WR04 ±1%, ±5%, Jumper

Thick Film General Purpose Chip Resistors Size 1210, 1206, 0805, 0603, 0402 RoHS 2 Compliant with exemption 7C-I Halogen free

*Contents in this sheet are subject to change without prior notice.

FEATURES

- 1. High reliability and stability
- 2. Reduced size of final equipment
- 3. Lower assembly costs
- 4. Higher component and equipment reliability
- 5. RoHS 2 compliant with exemption 7C-I and Halogen free products
- 6. Flammability against UL94-V0

APPLICATIONS

- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

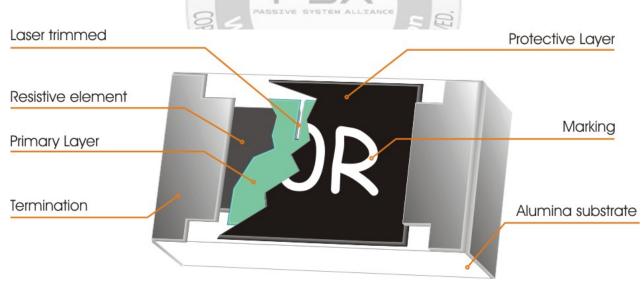


Fig 1. Construction of Chip-R

Approval sheet

QUICK REFERENCE DATA

Item		G	eneral Specificatio	on			
Series No.	WR10	WR12	WR08	WR06	WR04		
Size code	1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)		
Resistance Range		1Ω ~10MΩ (±5% tolerance), 1Ω~10MΩ (±1% tolerance),					
Resistance Tolerance		±1%, E96/E24 ±5%, E24					
TCR (ppm/°C) 10MΩ ≥R > 10Ω			≤ ± 100				
R≤10Ω			-200~+400				
Max. dissipation @ T _{amb} =70°C	1/2 W	1/4 W	1/8 W	1/10 W	1/16 W		
Max. Operation Voltage	200V	200V	150V	75V	50V		
Max. Overload Voltage	400V	400V	300∨	150V	100V		
Operation temperature		水石	-55 ~ +155°C				
Jote							

Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"

2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

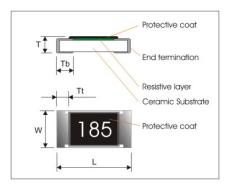
 $RCWV = \sqrt{Rated Power \times Resistance Value}$ or Max. RCWV listed above, whichever is lower.

For Jumper (0Ω) :

	Item		Ge	eneral Specification	า	
Series No.		WR10	WR12	WR08 🤤	WR06	WR04
Size code		1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)
Resistance	e Range	NO.		$50m\Omega$ for P tolerance $30m\Omega$ for F tolerance	-	
TCR (ppm	/°C)	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	TCR is not	applicable for Jump	er product	
Max. dissip @ T _{amb} =70		1/2 W	GCHIQIA W CORPO	1/8 W	1/10 W	1/16 W
P	Rated Current	3A	2A	1.6A	1A	1A
tolerance	Peak Current	7.5A	5A	3.2A	3A	2A
F	Rated Current	4A	2.9A	2A	1.8A	1A
tolerance	Peak Current	10A	5.8A	4A	3.6A	2A
Operation	temperature	-55 ~ +155°C				

DIMENSIONS (unit : mm)

	WR10	WR12	WR08	WR06	WR04
L	$\textbf{3.10}\pm\textbf{0.10}$	$\textbf{3.10}\pm\textbf{0.10}$	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
w	2.60 ± 0.10	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
Т	0.55 ± 0.10	0.55 ± 0.10	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.50 ± 0.20	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



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Nov- 2024



CATALOGUE NUMBERS

The resistors have a catalogue number starting with

WR12	X	472_	J	т	L
Size code	Type code	Resistance code	Tolerance code	Packaging code	Termination code
WR10 : 1210 WR12 : 1206 WR08 : 0805 WR06 : 0603 WR04 : 0402	X: Jumper ±5%. 1Ω ~ 10MΩ ±1% 10Ω ~ 1MΩ W: ±1% 1Ω <r<10ω ±1% 1MΩ<r<10mω< th=""><th>$\pm 5\%, E24: 2 \text{ significant} \\ \text{digits followed by no.} \\ \text{of zeros and a blank} \\ 4.7\Omega=4R7_{-} \\ 10\Omega=100_{-} \\ 220\Omega=221_{-} \\ \text{Remark: "_" means a blank} \\ \pm 1\%, E24+E96: \\ 3 \text{ significant digits} \\ \text{followed by no. of} \\ \text{zeros} \\ 102\Omega=1020 \\ 37.4K\Omega=3742 \\ 82\Omega=82R0 \\ \hline \text{For Jumper (0\Omega)} \\ \text{P tol. } \leq 50m\Omega=000_{-} \\ \text{F tol. } \leq 30m\Omega=0000 \\ \text{Remark: "_" means a blank} \\ \hline \end{tabular}$</th><th>F : ±1% J : ±5% For Jumper (0Ω) P : ≤ 50mΩ F : ≤ 30mΩ</th><th> T :7" Reeled taping Q :10" Reeled taping G :13" Reeled taping H :13" reel 50Kpcs only for 0402 B :Bulk D :7" reel 20Kpcs only for 0402 A :7" reel 15Kpcs only for 0402 </th><th>L:Sn base (lead-free)</th></r<10mω<></r<10ω 	$\pm 5\%, E24: 2 \text{ significant} \\ \text{digits followed by no.} \\ \text{of zeros and a blank} \\ 4.7\Omega=4R7_{-} \\ 10\Omega=100_{-} \\ 220\Omega=221_{-} \\ \text{Remark: "_" means a blank} \\ \pm 1\%, E24+E96: \\ 3 \text{ significant digits} \\ \text{followed by no. of} \\ \text{zeros} \\ 102\Omega=1020 \\ 37.4K\Omega=3742 \\ 82\Omega=82R0 \\ \hline \text{For Jumper (0\Omega)} \\ \text{P tol. } \leq 50m\Omega=000_{-} \\ \text{F tol. } \leq 30m\Omega=0000 \\ \text{Remark: "_" means a blank} \\ \hline \end{tabular}$	F : ±1% J : ±5% For Jumper (0Ω) P : ≤ 50mΩ F : ≤ 30mΩ	 T :7" Reeled taping Q :10" Reeled taping G :13" Reeled taping H :13" reel 50Kpcs only for 0402 B :Bulk D :7" reel 20Kpcs only for 0402 A :7" reel 15Kpcs only for 0402 	L:Sn base (lead-free)

Remark:

1210, 1206, 0805, 0603:

1. 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.

2. Bulk packaging: 5000pcs per poly-bag

0402:

- 1. 8mm width paper taping 10,000pcs per 7" reel, 70kpcs per 13" reel.
- 2. Bulk packaging: 5000pcs per poly-bag



MARKING

Size \ Nr. Of digit of code\tolerance	±5% & Jumper (0Ω)	±1%
1210 (3225)	3 digits marking	4 digits marking
1206 (3216)	3 digits marking	4 digits marking
0805 (2012)	3 digits marking	4 digits marking
0603 (1608)	3 digits marking	3 digits marking
0402 (1005)	NO MA	RKING

3 digits marking [$\pm 5\%$: 1210,1206, 0805, 0603 & Jumper (0 Ω)]

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

3 digits marking $(\pm 1\% : 0603)$

Nomina	al resistan	ce						Description							
1.E24 s	series	2 si	gnificant o	-	owed by I	No. of ze	eros .As 0	603 WR	06X ±5%						
			Exam			4.70	470			41/70	47140	170		1170	
				SISTAN		<u>4.7Ω</u> 4R7	47Ω 470		70Ω •71	4K70	47K0 473	470		M70 475	
			3 0	gits mar	king	487	470	4	.71	472	473	47	4 4	+/5	
2.E96 s	series	The	1st two d	ligit code	es are refe	erring to	the COD	E on the	table, the	e 3rd coo	de is the i	ndex of	resistanc	e value.	
		Rep	eat value	s betwe	en E24 ar	nd E96 s	eries, who	ose marl	king are b	based or	n the E96	CODE t	able.		
			Code	z	184	1. 1	x 胶	AA	в	+c	D	E		-	C
			-	10	³ 10 ⁻		∧	10 ⁰	10 ¹	10 ²	10 ³	10 ⁴) ⁵	10 ⁶
		I	Multiplier	10	HAD 10	<u>s</u>	0	10	<10 -	Rig	10	10	П	J	10
			Exam	ple	17 111	S			AV/	1	1				
			RE	SISTAN	CE '	1.78Ω	17.8Ω	. 17	78Ω	1K78	17K8	178	8K 1	M78	
			3 di	gits mar	king	25Y	25X	2	5A	25B	25C	25	D 2	25E	
0.0		The		a sel si se se se		PASSI	adan EQ4		ANCE		5				
3. Rem			re is no m	arking i	or the iter	ns not u	nder E24	and E90	series.		1				
	DE table:			13	20					. 25	1				1
CODE	R value	CODE	R-value	CODE	R-Value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value
01	100	13	133	25	178	37	237	49	316						
02	102									61	422	73	562	85	750
	-	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	14 15	137 140	26 27	182 187	39	243 249	50 51		1 days					
03 04	105 107		-	-	-		-		324	62	432	74	576	86	768
		15	140	27	187	39	249	51	324 332	62 63	432 442	74 75	576 590	86 87	768 787
04	107	15 16	140 143	27 28	187 191	39 40	249 255	51 52	324 332 340	62 63 64	432 442 453	74 75 76	576 590 604	86 87 88	768 787 806
04 05	107 110	15 16 17	140 143 147	27 28 29	187 191 196	39 40 41	249 255 261	51 52 53	324 332 340 348	62 63 64 65	432 442 453 464	74 75 76 77	576 590 604 619	86 87 88 89	768 787 806 825
04 05 06	107 110 113	15 16 17 18	140 143 147 150	27 28 29 30	187 191 196 200	39 40 41 42	249 255 261 267	51 52 53 54	324 332 340 348 357	62 63 64 65 66	432 442 453 464 475	74 75 76 77 78	576 590 604 619 634	86 87 88 89 90	768 787 806 825 845
04 05 06 07	107 110 113 115	15 16 17 18 19	140 143 147 150 154	27 28 29 30 31	187 191 196 200 205	39 40 41 42 43	249 255 261 267 274	51 52 53 54 55	324 332 340 348 357 365	62 63 64 65 66 67	432 442 453 464 475 487	74 75 76 77 78 79	576 590 604 619 634 649	86 87 88 89 90 91	768 787 806 825 845 866
04 05 06 07 08	107 110 113 115 118	15 16 17 18 19 20	140 143 147 150 154 158	27 28 29 30 31 32	187 191 196 200 205 210	39 40 41 42 43 44	249 255 261 267 274 280	51 52 53 54 55 56	324 332 340 348 357 365 374	62 63 64 65 66 67 68	432 442 453 464 475 487 499	74 75 76 77 78 79 80	576 590 604 619 634 649 665	86 87 88 89 90 91 92	768 787 806 825 845 866 887
04 05 06 07 08 09	107 110 113 115 118 121	15 16 17 18 19 20 21	140 143 147 150 154 158 162	27 28 29 30 31 32 33	187 191 196 200 205 210 215	39 40 41 42 43 43 44 45	249 255 261 267 274 280 287	51 52 53 54 55 56 57	324 332 340 348 357 365 374 383	62 63 64 65 66 67 68 69	432 442 453 464 475 487 499 511	74 75 76 77 78 79 80 81	576 590 604 619 634 649 665 681	86 87 88 89 90 91 92 93	768 787 806 825 845 866 887 909

4 digits marking $(\pm 1\% : 1210, 1206, 0805)$

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value. For values below $97\Omega6$ the R is used as a digit. For values of 100Ω or greater, the first 3 digits are significant, and the fourth digit indicates the number of multiple to follow.

Example

RESISTANCE	Jumper (0 Ω)	4.7Ω	10Ω	12Ω	100Ω	6800Ω	47000Ω	470000Ω
3 digits marking (1210, 1206, 0805, 0603 ±5% & Jumper)	000	4R7	100	120	101	682	473	474
4 digits marking	-	4R70	10R0	12R0	1000	6801	4702	4703

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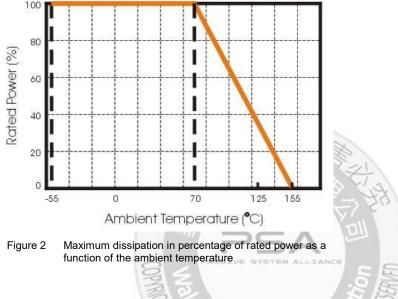
FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of \pm 5%, and E96 series for resistors with a tolerance of \pm 1%. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2



MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

Storage and Handling Conditions:

- 1. Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
- 2. To store products with following condition:

Temperature :5 to 40°C

- Humidity :20 to 70% relative humidity
- 3. Caution:
 - a. Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.
 - It may cause oxdization of electrode, which easily be resulted in poor soldering.
 - b. To store products on the shelf and avoid exposure to moisture.
- c. Don't expose products to excessive shock, vibration, direct sunlight and so on

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SOLDERING CONDITION follows J-STD-020D

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds.

Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

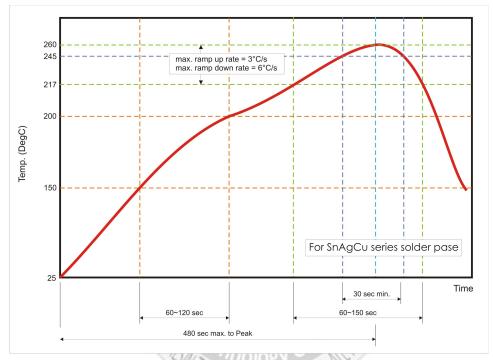


Fig 3. Recommended IR reflow soldering profile for SMT process with SnAgCu series solder paste



TESTS AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range : Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

All soldering tests are performed with midly activated flux.

тгот		REQUIREMENT	
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} t_1 : 20^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}; \ t_2 : -$	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	
	$F_1(2^{-1}, 1)$ 55° C or +155°C R_1 : Resistance at reference temperature (20°C+5°C/-1°C) R_2 : Resistance at test temperature (-55°C or +155°C)	SABRAVED.	<50mΩ
Resistance to soldering heat(R.S.H) JISC5201-1:1998 Clause 4.18	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 260°C ±5°C	±5%:ΔR/Rmax.±(1%+0.05Ω) ±1%:ΔR/Rmax.±(0.5%+0.05Ω) no visible damage	<50mΩ
Solderability JISC5201-1:1998 Clause 4.17	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235°C±5°C	95% coverage min., good tinning visible damage	g and no
Temperature cycling JISC5201-1:1998 Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C- 1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	±5%: ΔR/R max. ±(1%+0.05Ω) ±1%:ΔR/Rmax.±(0.5%+0.05Ω) No visible damage	<50mΩ
High Temperature Exposure MIL-STD-202 method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	±5%:∆R/Rmax.±(2%+0.1Ω) ±1%:∆R/Rmax.±(1%+0.1Ω) No visible damage	<50mΩ

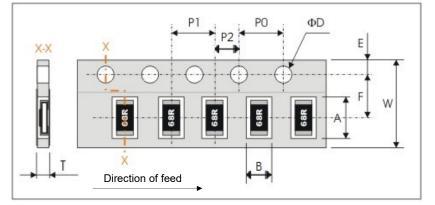


TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
IESI	PROCEDURE / TEST METHOD	Resistor	0Ω
Bending strength JISC5201-1:1998 Clause 4.33	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 3mm for 10sec, 5mm for WR04	±5%:∆R/Rmax.±(1%+0.05Ω) ±1%:∆R/Rmax.±(1%+0.05Ω) No visual damaged	<50mΩ
Adhesion JISC5201-1:1998 Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or re the terminations	moval of
Short Time Overload (STOL) JISC5201-1:1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	±5%: ΔR/R max. ±(2%+0.05Ω) ±1%: ΔR/R max. ±(1%+0.05Ω) No visible damage	<50mΩ
Load life in Humidity JISC5201-1:1998 Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	±5%: ΔR/R max. ±(2%+0.1Ω) ±1%: ΔR/R max. ±(1%+0.1Ω) No visible damage	<50mΩ
Load life (endurance) JISC5201-1:1998 Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	±5%: ΔR/R max. ±(3%+0.1Ω) ±1%: ΔR/R max. ±(1%+0.1Ω) No visible damage	<50mΩ
Insulation Resistance JISC5201-1:1998 Clause 4.6	Apply the maximum overload voltage (DC) for 1minute	R≥10GΩ	
Dielectric Withstand Voltage JISC5201-1:1998 Clause 4.7	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover	



PACKAGING

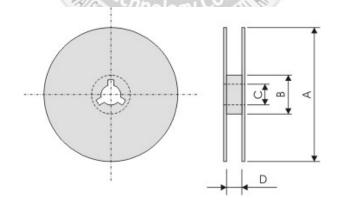
Paper Tape specifications (unit :mm)



Series No.	А	В	W	F	E
WR10	3.60±0.20	3.00±0.20			
WR12	3.60±0.20	2.00±0.20			
WR08	2.40±0.20	1.65±0.20	8.00±0.30	3.50±0.20	1.75±0.10
WR06	1.90±0.20	1.10±0.20			
WR04	1.20±0.10	0.70±0.10	百瓜竹		

	FAIT -		14 - Y	5	
Series No.	P1	P0	P2	ΦD	Т
WR10/12/08	4.00+0.40	U N			0.80±0.1
WR06	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	0.70±0.05
WR04	2.00±0.10		lio,	RVE	0.50±0.05

Reel dimensions



Symbol	A	В	С	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Ф330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5